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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/722,365	11/25/2003	Osamu Omori	81754.0106	81754.0106 4601 EXAMINER	
26021 7	590 06/02/2005		EXAM		
HOGAN & HARTSON L.L.P.			VU, HUNG K		
500 S. GRAND AVENUE SUITE 1900			ART UNIT	PAPER NUMBER	
LOS ANGELES, CA 90071-2611			2811		
			DATE MAILED: 06/02/2009	DATE MAILED: 06/02/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
		10/722,365	OMORI, OSAMU			
	Office Action Summary	Examiner	Art Unit			
		Hung Vu	2811			
Period fo	The MAILING DATE of this communication ap or Reply	ppears on the cover sheet with the c	orrespondence address			
THE - Exter after - If the - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REPL MAILING DATE OF THIS COMMUNICATION, nsions of time may be available under the provisions of 37 CFR 1. SIX (6) MONTHS from the mailing date of this communication, o period for reply specified above is less than thirty (30) days, a reply period for reply is specified above, the maximum statutory period re to reply within the set or extended period for reply will, by staturely received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be tin ply within the statutory minimum of thirty (30) day if will apply and will expire SIX (6) MONTHS from te, cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).			
Status						
1)[Responsive to communication(s) filed on 07 I	<u>March 2005</u> .				
2a)⊠	This action is FINAL 2b) This	is action is non-final.				
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposit	ion of Claims					
5)□ 6)⊠ 7)⊠	 ✓ Claim(s) 12-23 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. Claim(s) is/are allowed. ✓ Claim(s) 12-18 and 23 is/are rejected. ✓ Claim(s) 19-22 is/are objected to. Claim(s) are subject to restriction and/or election requirement. 					
Applicat	ion Papers					
10)	The specification is objected to by the Examination The drawing(s) filed on is/are: a) acceptable and applicant may not request that any objection to the Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Examination is objected.	ccepted or b) objected to by the e drawing(s) be held in abeyance. Se ction is required if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).			
Priority (under 35 U.S.C. § 119					
a)	Acknowledgment is made of a claim for foreig All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority application from the International Bures See the attached detailed Office action for a list	nts have been received. nts have been received in Applicat fority documents have been receiv au (PCT Rule 17.2(a)).	ion No ed in this National Stage			
	ce of References Cited (PTO-892)	4) Interview Summary				
3) 🔲 Infor	ce of Draftsperson's Patent Drawing Review (PTO-948) rmation Disclosure Statement(s) (PTO-1449 or PTO/SB/0 er No(s)/Mail Date	Paper No(s)/Mail D 5) Notice of Informal I 6) Other:	ate Patent Application (PTO-152)			

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DETAILED ACTION

Claim Objections

1. Claims 19 and 23 are objected to because of the following informalities:

In claim 19, line 2, "16" should be changed to "18", for clarity.

In claim 23, line 6, "the substrate" should be changed to "a substrate", for clarity.

Appropriate correction is required.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 12, 15-18 and 23 are rejected under 35 U.S.C. 102(b) as being anticipated by Yamanaka (PN 5,998,862).

Yamanaka discloses, as shown in Figures 3A-22, a semiconductor device comprising:

a substrate (upper portion of 21) having an electrode (23, portion of 24 or 28) formed thereon;

a cover (29) affixed to the substrate, the cover having a first opening (step 31a) with an adhesive (32) disposed therein for affixing the cover to the substrate, and a second opening (30) that received and surrounds the electrode when the cover is affixed to the substrate, the first opening configured so that adhesive is present only in the first opening and not in the second opening.

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Regarding claim 15, Yamanaka discloses an electronic equipment comprising the semiconductor

device.

Regarding claim 16, Yamanaka discloses, as shown in Figures 3A-22, a cover (29) for a

semiconductor device having a substrate (21) with an optical unit (2) formed thereon, the cover

comprising:

a portion defining a first opening (step 31a) on a surface of the cover;

a portion defining a second opening (30) on the surface of the cover;

a translucent portion in at least the portion, defining the second opening, wherein the first

opening is formed along a circumference of the second opening, and wherein the first opening

contains an adhesive for affixing the cover to the substrate, and wherein the second opening is

formed to receive and surround the optical unit inside of the second opening when the cover is

affixed to the substrate, and wherein the second opening that includes the optical unit is sealed

from the adhesive in the first opening [Figure 3B].

Regarding claim 17, Yamanaka discloses the first opening is formed so as to surround the second

opening.

Regarding claim 18, Yamanaka discloses the cover further comprising: a portion defining a third

opening (step 31b, 31c) on the surface that includes the portion defining the first opening and the

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portion defining the second opening and wherein the third opening is formed along a circumference of the second opening [Figure 2G].

Regarding claim 23, Yamanaka discloses, as shown in Figures 3A-22, a cover (29) for a semiconductor device, comprising:

a portion defining a first opening (step 31a) on a surface of the cover;

a portion defining a second opening (30) on the surface of the cover;

a translucent portion in a least the portion defining the second opening, wherein the first opening is formed along a circumference of the second opening and receives an adhesive (32) for affixing the cover to a substrate, the adhesive being sealed within the first opening and not present in the second opening, and the second opening is formed to include an optical unit (2) inside of the second opening.

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 13 and 14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamanaka (PN 5,998,862) in view of Zu et al. (PN 6,594,153).

Regarding claim 13, Yamanaka discloses the claimed invention including the semiconductor device as explained in the rejection above. Yamanaka does not disclose the device further

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comprising a support member attached to the semiconductor device. However, Zu et al. discloses a semiconductor device comprising a support member (107) attached to the semiconductor device (101). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to form the device of Yamanaka further comprising a support member attached to the semiconductor device, such as taught by Zu et al. in order to reduce the noise interference and to improve circuit performance.

Regarding claim 14, Yamanaka discloses the claimed invention including the semiconductor as explained in the rejection above. Yamanaka does not disclose the device further comprising a circuit substrate that is electrically coupled to the electrode of the semiconductor device.

However, Zu et al. discloses a semiconductor device (101) comprising a circuit substrate (111) that is electrically coupled to the electrode (121) of the semiconductor device. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to form the device of Yamanaka further comprising a circuit substrate that is electrically coupled to the electrode of the semiconductor device, such as taught by Zu et al. in order to connect the semiconductor device with other circuit elements.

Allowable Subject Matter

4. Claims 19-22 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

5. The following is an examiner's statement of reasons for allowance:

Applicant's claims 19-22 are allowable over the references of record because none of these references disclose or can be combined to yield the claimed cover comprising the first opening is formed between the second opening and the third opening, as recited in claim 19; a portion defining a through-hole that extends from a back side of the surface that includes the portion defining the first opening to inside the first opening, as recited in claim 20; the first opening is formed so as to penetrate from one side of the cover to the other side of the cover, as recited in claim 22.

Response to Arguments

6. Applicant's arguments with respect to claims 12, 16 and 23 have been considered but are most in view of the new ground(s) of rejection.

Conclusion

7. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37

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CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hung K. Vu whose telephone number is (571) 272-1666. The examiner can normally be reached on Mon-Thurs 6:00-3:30, alternate Friday 7:00-3:30, Eastern Time.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie C. Lee can be reached on (571) 272-1732. The Central Fax Number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Vu

May 26, 2005

Hung Vu

Primary Examiner